

CLAIMS

5 What is claimed is:

1. A convolutional interleaver/deinterleaver, comprising:  
a memory array having plural array cells, selected array  
cells storing symbols therein, the memory array having an memory  
block length,  $N$ , and an interleave depth,  $D$ ;

10 a write commutator, operably connected with the memory  
array, for writing a symbol  $B_i$  to a preselected memory cell at a  
first time;

a read commutator, operably connected with the memory array,  
for reading a symbol  $B_i$  from the preselected memory cell at a  
15 second time, wherein a difference between the second time and the  
first time is a predetermined delay, the predetermined delay  
being related a desired structure of the memory array.

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